



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor application of:

Examiner:

Tsutomu IMOTO

Application No.: 10/689,305

Group Art Unit:

Filed: October 20, 2003

Attorney Docket No.: 09792909-5755

For: Method of Fabricating Semiconductor
Device Having Semiconductor Resistance
Element

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION UNDER 37 CFR 1.182

Applicant respectfully requests that the enclosed drawing sheet 5/5 incorporating Figures 5 and 6 be entered into the above-identified patent application. This drawing sheet was filed in parent application number 09/862,042, on May 21, 2001, from which the subject application claims priority.

Preliminary Amendment Pursuant to 1.53(b) was submitted with the initial filing which amended the Related Application Data to indicate that the subject application is a divisional of co-pending U.S. application number 09/862,042, filed on May 21, 2001, which claims priority to Japanese application number P2000-153445, filed May 24, 2000.

At filing, Applicant also provided the Office with the Declaration and Power of Attorney that was filed in parent application serial number 09/862,042 (CFR 1.63(d)).

Applicant respectfully requests that Figures 5 and 6 be entered and October 20, 2003, remain the filing date of this application.

Respectfully submitted,

Dated: March 11, 2004

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